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### ⑤4 Transcoding device.

⑤ Known transcoders for compressed video signals comprise a decoder and a coder coupled thereto, between which a data signal is transmitted. Because compressed video signals comprise so-called difference pictures and normal pictures, both the decoder and the coder should have a feedback loop which is utilized in decoding and coding a difference picture and is not utilized in decoding and coding a normal picture. For example, for the purpose of

prediction means (motion compensation and motion estimation) present in the feedback loop of the coder, much computation has to be carried out in the coder. The computation required is considerably reduced by concomitantly transmitting one or more information signals in addition to the data signal in a transcoder according to the invention, and complete decoding and complete coding no longer takes place.

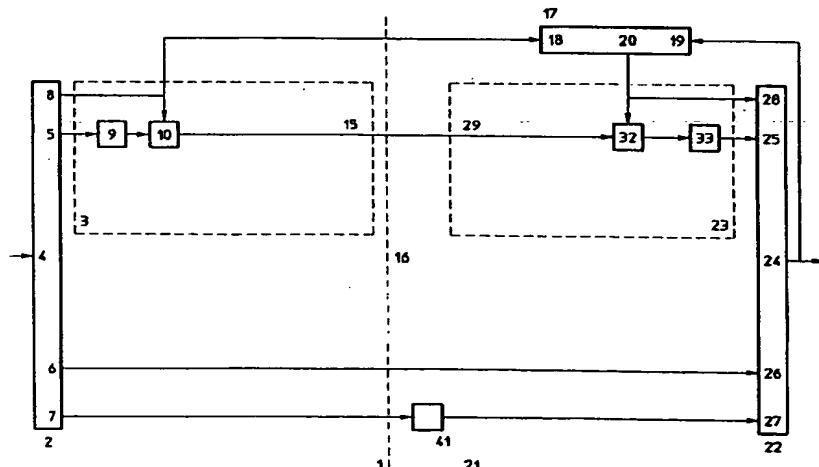


Fig. 2

#### A. Background of the invention

The invention relates to a transcoding device comprising a decoding section for

- receiving a first coded bit stream, and
- converting the first coded bit stream into at least one data signal,

and comprising a coding section for

- receiving at least one data signal, and
- converting at least one data signal into a second coded bit stream,

which transcoding device has a coupling, situated between the decoding section and the coding section, for transporting at least one data signal.

Such a transcoding device is generally known, the decoding section being formed by a decoder and the coding section being formed by a coder. The decoder converts the first coded bit stream to be received, which represents a compressed video signal, into a data signal by decoding said bit stream. The coder converts this data signal to be received into the second coded bit stream, which represents the video signal in another way, for example in accordance with another standard or by only reducing the bit rate, by coding said data signal. Between the decoder and the coder there is the coupling for transporting the data signal which represents the video signal per se in uncoded form.

Because compressed video signals comprise so-called difference pictures and normal pictures, both the decoder and the coder should have a feedback loop which is utilized in decoding and coding a difference picture and is not utilized in decoding and coding a normal picture. For example, for the purpose of prediction means (motion compensation and motion estimation) present in the feedback loop of the coder, much computation has to be carried out in the coder.

Such a known transcoding device has, inter alia, the disadvantage that, for example, much computation has to be carried out in the coding section.

#### B. Summary of the invention

The object of the invention is, inter alia, to provide a transcoding device of the type mentioned in the preamble, in which the computation required is considerably reduced.

For this purpose, the transcoding device according to the invention has the characteristic that it has a further coupling, situated between the decoding section and the coding section, for transporting at least one information signal.

Because there is the further coupling between the decoding section and the coding section for transporting the information signal, information

present in said information signal no longer has to be combined with the data signal in the decoding section and information present in said information signal no longer has to be computed completely in the coding section. At the same time, complete decoding no longer takes place in the decoding section and complete coding no longer takes place in the coding section, thereby economizing on computation.

The invention is based, inter alia, on the insight that certain operations take place in a decoder and that corresponding inverse operations take place in a coder, and the sum total of these operations can be dispensed with if the decoder and the coder are

coupled to each other. In this case, it is necessary to speak of a decoding section instead of a decoder and of a coding section instead of a coder because complete decoding and coding no longer take place and the data signal transported between the decoding section and the coding section no longer represents per se the video signal in uncoded form, but said data signal transported between the decoding section and the coding section via the coupling represents, together with the information signal transported between the decoding section and the coding section via the further coupling, the video signal in another form which is still to some extent coded.

Of course, the coupling and the further coupling may be identical, for example as a result of multiplexing the data signal and the information signal by means of a multiplexer disposed in the decoding section and demultiplexing by means of a demultiplexer disposed in the coding section. An essential feature of the transcoding device according to the invention, however, remains the fact that the data signal transported between the decoding section and the coding section and information signal together represent the video signal in another form which is still to some extent coded, whereas, in the abovementioned, generally known transcoding device, the other data signal transported between the decoder and the coder represents the video signal in uncoded form.

A first embodiment of the transcoding device according to the invention has the characteristic that the decoding section is provided with

- data reprocessing means for generating a data signal, and
- demultiplexing means having an input for receiving the first coded bit stream and having a first output coupled to the data reprocessing means and having a second output for generating an information signal,

the coding section being provided with

- data processing means for receiving a data signal, and
- multiplexing means having a first input coupled to the data processing

means and having a second input for receiving an information signal and having an output for generating the second coded bit stream, the coupling being disposed between the data reprocessing means and the data processing means, and the further coupling being disposed between the second output of the demultiplexing means and the second input of the multiplexing means.

The demultiplexing means selects, from the first coded bit stream, the information signal generated at the second output and, from the first coded bit stream, a signal which is generated at the first output and which is converted into the data signal by means of the data reprocessing means. The multiplexing means combines the information signal fed to the second input and another signal, which is fed to the first input and which is obtained by converting the data signal by means of the data processing means, to form the second coded bit stream.

A second embodiment of the transcoding device according to the invention has the characteristic that the data reprocessing means comprise a control input which is coupled to the second output, and the data processing means comprise a control input which is coupled to the second input.

Via the control inputs, the data reprocessing means and the data processing means are provided with information signals.

A third embodiment of the transcoding device according to the invention has the characteristic that the data reprocessing means comprise decoding means and inverse quantization means, and the data processing means comprise further quantization means and coding means.

In accordance with such a transcoding device, the second coded bit stream represents the video signal in accordance with the same standard as the first coded bit stream, but at a lower bit rate.

A fourth embodiment of the transcoding device according to the invention has the characteristic that the data reprocessing means comprise inverse transformation means and prediction means disposed in a feedback loop, and the data processing means comprise (further) transformation means and (further) prediction means disposed in a feedback loop containing therein inverse further quantization means and inverse (further) transformation means.

In accordance with such a transcoding means, the second coded bit stream represents the video signal in accordance with the same standard, but with one or more modified coding parameters, such as picture size, picture frequency or picture coding procedure, or in accordance with a standard other than the first coded bit stream.

5 A fifth embodiment of the transcoding device according to the invention has the characteristic that the information signal is a quantization control signal, the second output being a quantization control signal output and the second input a quantization control signal input.

10 In this embodiment, the information signal is formed by the quantization control signal which is fed to the data reprocessing means (inverse quantization means) via a control input from the quantization control signal output of the demultiplexing means. Furthermore, the quantization signal is optionally processed, for example fed via a control unit to a control input of the data processing means (quantization means) and to the quantization control signal input of the multiplexing means. Because the quantization control signal is fed to the coding section, it no longer needs to be derived from the data signal, for which purpose much computation was necessary.

15 A sixth embodiment of the transcoding device according to the invention has the characteristic that the information signal is a vector signal, the second output being a vector output and the second input a vector input.

20 In this embodiment, the information signal is formed by the vector signal which is fed to the data reprocessing means (prediction means) via a control input from the vector output of the demultiplexing means. Furthermore, the vector signal is optionally processed, for example fed via a translation unit to a control input of the data processing means (further prediction means) and to the vector input of the multiplexing means. In this case, the vector signal no longer has to be computed completely in the coding section because it is fed thereto.

25 A seventh embodiment of the transcoding device according to the invention has the characteristic that the information signal is a mode signal, the second output being a mode output and the second input a mode input.

30 In this embodiment, the information signal is formed by the mode signal which is fed to the data reprocessing means (prediction means) via a control input from the mode output of the demultiplexing means. Furthermore, the mode signal is optionally processed, for example fed via a translation/determination unit to a control input of the data processing means (further prediction means) and to the mode input of the multiplexing means. In this case, the mode signal no longer needs to be completely computed in the coding section because it is fed thereto.

35 An eighth embodiment of the transcoding device according to the invention has the characteristic that the information signal comprises a vector signal and a mode signal, the second output comprising a vector output and a mode output and the

second input comprising a vector input and a mode input.

This eighth embodiment is a combination of the sixth and the seventh embodiment.

A ninth embodiment of the transcoding device according to the invention has the characteristic that the information signal comprises a quantization control signal, a vector signal and a mode signal, the second output comprising a quantization control signal output, a vector output and a mode output and the second input comprising a quantization control signal input, a vector input and a mode input.

This ninth embodiment is a combination of the fifth and the eighth embodiment.

Obviously, two or more of the embodiments mentioned may be combined in all possible ways.

#### C. References

- Dutch Patent Application No. 9200499 (not pre-published)
- Dutch Patent Application No. 9201594 (not pre-published)

Both Dutch patent applications are regarded as incorporated in this application by means of reference:

#### D. Exemplary embodiment

The invention will be explained in greater detail with reference to an exemplary embodiment shown in the figures. In the latter:

Fig. 1 shows a more complex version of a transcoding device according to the invention, and

Fig. 2 shows a less complex version of a transcoding device according to the invention.

The transcoding device, or transcoder, shown in Fig. 1 is made up of a decoding section 1 and a coding section 21, which are mutually separated by broken line 16. Decoding section 1 comprises demultiplexing means, or demultiplexer, 2 and data reprocessing means 3. Demultiplexer 2 has an input 4 for receiving a first coded bit stream, a first output 5 for coupling to an input of data reprocessing means 3, and a second output which is made up of a vector output 6 for generating a vector signal, a mode output 7 for generating a mode signal, and a quantization control signal output 8 for generating a quantization control signal. Data reprocessing means 3 are provided with decoding means, or VLD (variable length decoder), 9 an input of which is coupled to output 5, and with inverse quantization means 10, an input of which is coupled to an output of VLD 9 and a control input of which is coupled to quantization control signal output 8. Furthermore, data reprocessing means 3

are provided with inverse transformation means 11, an input of which is coupled to an output of inverse quantization means 10, with a combining device 12 (addition circuit), a first input of which is coupled to an output of transformation means 11, with memory means 13, an input of which is coupled to an output of combining device 12, and with prediction means 14, an input of which is coupled to an output of memory means 13 and an output of which is coupled to a second input of combining device 12 and a first control input of which is coupled to vector output 6 and a second control input of which is coupled to mode output 7. The output of combining device 12 is furthermore coupled to an output 15 of data reprocessing means 3.

Coding section 21 comprises multiplexing means, or multiplexer, 22 and data processing means 23. Multiplexer 22 has a first input 25 for coupling to an output of data processing means 23, a second input which is made up of a vector input 26 for receiving a vector signal, a mode input 27 for receiving a mode signal, and a quantization control signal input 28 for receiving a quantization control signal, and an output 24 for generating a second coded bit stream. Data processing means 23 are provided with quantization means 32, and with coding means, or VLC (variable length coder), 33 an input of which is coupled to an output of quantization means 32 and an output of which is coupled to input 25. Furthermore, data processing means 23 are provided with a combining device 30 (subtraction circuit), a first input of which is coupled to an input 29 of data processing means 23, with transformation means 31, an input of which is coupled to an output of combining device 30 and an output of which is coupled to an input of quantization means 32, with inverse quantization means 34, an input of which is coupled to the output of quantization means 32, with inverse transformation means 35, an input of which is coupled to an output of inverse quantization means 34, with combining circuit 36 (addition circuit), a first input of which is coupled to an output of inverse transformation means 35, with memory means 37, an input of which is coupled to an output of combining device 36, and with prediction means 38; an input of which is coupled to an output of memory means 37 and an output of which is coupled to a second input of combining device 30 and to a second input of combining device 36 and a first control input of which is coupled to vector input 26 and a second control input of which is coupled to mode input 27.

Output 15 and input 29 are mutually connected via coupling 39, which coupling is, in its simplest form, a through connection, and, in a more complex form, is, for example, a post processor known to the person skilled in the art or, for example, is a temporal and/or spatial converter known to the per-

son skilled in the art. Vector output 6 and vector input 26 are mutually connected via a further coupling 40 and mode output 7 and mode input 27 are mutually connected via a further coupling 41. A simplest form of further coupling 40 is a through connection, whereas a more complex form is, for example, a translation unit 40 known to the person skilled in the art, the vector signal of the vector output 6 being converted into another vector signal for vector input 26 in order to make it possible, for example, to translate from one standard into another standard, it sometimes being necessary to adjust the accuracy and/or range of the vector information. There is also the possibility that different vector signals have to be combined to form one vector signal. A simplest form of further coupling 41 is a through connection, whereas a more complex form is, for example, a translation/determination unit 41 known to the person skilled in the art, the mode signal of mode output 7 being converted into another mode signal for mode input 27 in order to make it possible, for example, to translate one standard into another standard, it sometimes being necessary to redetermine the mode signal, as has to be done, correspondingly, in separate encoders. If the temporal and/or spatial converter 39 is used vector information and/or mode information is economized on to a greater or lesser extent per interval of time, the translation unit and translation/determination unit then having to be adjusted by converter 39 via connections not shown in Fig. 1.

Control unit 17, which could also be regarded as a further coupling, is provided with an input 18, coupled to quantization control signal output 8, for receiving the quantization control signal, with an output 19, coupled to output 24, for receiving the second coded bit stream, and with an output 20, coupled to input 28 and to a control input of quantization means 32 and to a control input of inverse quantization means 34, for supplying a further quantization control signal. Control unit 17 furthermore generally has a preset input, not shown in Fig. 1, for setting an adjustable ratio, the so-called input/output bit rate.

The operation of the transcoder shown in Fig. 1 is as follows. Multiplexer 2 receives, at input 4, the first coded bit stream, which is split up into a signal which is fed via output 5 to VLD 9, into a vector signal which is fed via vector output 6 to the first control input of prediction means 14 and to further coupling (translation unit) 40, into a mode signal which is fed via mode output 7 to the second control input of prediction means 14 and to further coupling (translation/determination unit) 41, and into a quantization control signal which is fed via quantization output 8 to the control input of inverse quantization means 10 and to input 18 of control

unit 17. VLD 9 performs a decoding, for example on the basis of a table, after which inverse quantization means 10 perform an inverse quantization and inverse transformation means 11 perform an inverse transformation, for example an inverse discrete cosine transformation. In this way, a data signal is obtained which is the difference between a particular group of picture elements, or pixels, of a particular picture and a particular group of pixels of a picture preceding it. With the aid of the feedback loop containing therein the prediction means 14 (motion compensation means) and the memory means 13, the particular group of pixels of the particular picture then appears as a data signal at output 15. If account is taken of movements in the picture content of consecutive pictures during coding, this is compensated for by prediction means 14 (motion compensation means) in response to the vector signal and/or the mode signal. From output 15, said data signal flows (possibly via the temporal and/or spatial converter 39 to economize on vector information and/or mode information) to input 29, a predetermined number of bits thus forming a picture element or pixel. Assuming that the capacity of memory means 37 is unoccupied, a first group of pixels reaches, via combining device 30, the transformation means 31, which perform, for example, a discrete cosine transformation on the group of pixels, the associated coefficient being determined for each frequency component. The quantization means 32 quantize the signal obtained. The quantized signal is then coded by VLC 33, for example on the basis of a table with which new code words are generated which, on average, have a shorter length than the incoming words, and is fed to input 25 of multiplexer 22. This transformed, quantized and coded first group of pixels consequently forms the first section of the coded signal. After being transformed and quantized, the first group of pixels is inversely quantized by inverse quantization means 34 and inversely transformed by inverse transformation means 35, and stored at a first location in memory means 37 via combining device 36. A second group of pixels traverses the same route as the first group of pixels, undergoes the same operations and is stored at a second location in memory means 37, etc., until all the groups of pixels of one complete picture (the first picture) have been stored. The first group of pixels of the subsequent (second) picture is then presented to input 29. In this connection, it is conceivable that prediction means 38 (motion estimation means) make a prediction (estimation), on the basis of a connection to input 29 not shown in Fig. 1, of a possible movement in the picture in order to improve the quality of the coding. At the same time, (the prediction of) the first group of pixels of the preceding (first) picture is fed via

prediction means 38 to the first group of pixels of the (second) picture to be coded via combining device 30. Because the difference between the first group of pixels of the second picture and the first group of pixels of the first picture is then presented to data processing means 23, the coding proceeds appreciably more efficiently. Taking account of any movements in the picture content of consecutive pictures by means of the prediction means 38 increases the efficiency still further. Instead of vectors, other prediction parameters determined on the basis of other prediction methods can, of course, be transmitted.

What is described above is the operation of the transcoder in regard to a first layer which is shown in Fig. 1 and which is built up around data reprocessing means 3 and data processing means 23. The operation in regard to a second layer, which is not shown in Fig. 1, for the purpose of pyramidal or layered coding and decoding and built up around further reprocessing means and further data processing means is, in principle, identical and is described comprehensively in the references mentioned.

According to the transcoder shown in Fig. 1, the second coded bit stream represents the video signal in accordance with the same standard as the first coded bit stream, but with one or more modified coding parameters, such as picture size, picture frequency and picture coding procedure, or in accordance with a standard other than the first coded bit stream.

In the case of conventional encoders, there is no input 18 in control unit 17 for receiving the quantization control signal from quantization control signal output 8. Furthermore, the further couplings 40 and 41 are not present in such cases, and each information signal (quantization control signal, vector signal, mode signal) is recomputed in the coding section, the data signal to be transported via coupling 39 representing the video signal per se in uncoded form. The computation takes place on the basis of said data signal.

Owing to the passing of information signals from the decoding section 1 to the coding section 21, the latter no longer have to be computed in the coding section 21, which thus economizes on computation.

If the transcoder according to the invention is regarded as a linear system, a simplified version of control unit 17 operates as follows. Because it is approximately true that (decoding section quantization)/(coding section quantization) = (outgoing data)/(incoming data), that (incoming data + incoming information)/(outgoing data + outgoing information) = (adjustable ratio) and that (incoming information) = outgoing information, all the parameters being known or having to be set except

(coding section quantization) and (outgoing data), control unit computes these two unknown parameters on the basis of the said equations, after which the coding section is fed a certain quantization control signal. A subsequent (partial) coding is performed with the latter and this leads to new outgoing data, which are fed back to control unit 17. This diagnoses a certain error, which results in further adjustment in the computation of the subsequent quantization control signal.

The transcoder device, or transcoder, shown in Fig. 2 is made up of a decoding section 1 and a coding section 21, mutually separated by broken line 16. Decoding section 1 comprises demultiplexing means, or demultiplexer, 2 and data reprocessing means 3. Demultiplexer 2 has an input 4 for receiving a first coded bit stream, a first output 5 for coupling to an input of data reprocessing means 3, and a second output which is made up of a vector output 6 for generating a vector signal, a mode output 7 for generating a mode signal, and a quantization control signal output 8 for generating a quantization control signal. Data reprocessing means 3 are provided with decoding means, or VLD (variable length decoder), 9 an input of which is coupled to output 5, and with inverse quantization means 10, an input of which is coupled to an output of VLD 9 and a control input of which is coupled to quantization control signal output 8 and an output of which is coupled to an output 15 of data reprocessing means 3.

Coding section 21 comprises multiplexing means, or multiplexer, 22 and data processing means 23. Multiplexer 22 has a first input 25 for coupling to an output of data processing means 23, a second input which is made up of a vector input 26 for receiving a vector signal, a mode input 27 for receiving a mode signal, and a quantization control signal input 28 for receiving a quantization control signal, and an output 24 for generating a second coded bit stream. Data processing means 23 are provided with quantization means 32, an input of which is coupled to an input 29 of data processing means 32 and a control input of which is coupled to quantization control signal input 28, and with coding means, or VLC (variable length coder), 33 an input of which is coupled to an output of quantization means 32 and an output of which is coupled to input 25.

Output 15 and input 29 are mutually connected via a coupling 39, which is not shown in Fig. 2, which coupling is in this case a through connection. Vector output 6 and vector input 26 are mutually connected via a further coupling 40, which is not shown in Fig. 2, and mode output 7 and mode input 27 are mutually connected via a further coupling 41. In this case, further coupling 40 is a through connection, whereas further coupling 41 is

a translation/determination unit 41 known to the person skilled in the art, the mode signal of mode output 7 being converted into another mode signal for mode input 27 because, even if no translation is necessary from one standard to another standard, it is nevertheless sometimes necessary to redetermine the mode signal, as occurs, correspondingly, in separate encoders, all this being dependent on the data processing means 23.

Control unit 17, which could also be regarded as a further coupling, is provided with an input 18, coupled to the quantization control signal output 8, for receiving the quantization control signal, with an input 19, coupled to output 24, for receiving the second coded bit stream, and with an output 20, coupled to the quantization control signal input 28 and to a control input of quantization means 32, for supplying a further quantization control signal. Control unit 17 furthermore generally has a preset input, not shown in Fig. 2, for setting an adjustable ratio, the so-called input/output bit rate.

The operation of the transcoder shown in Fig. 2 is as follows. Multiplexer 2 receives, at input 4, the first coded bit stream, which is split up into a signal which is fed via output 5 to VLD 9, into a vector signal which is fed via vector output 6 to the coding section 21, into a mode signal which is fed via mode output 7 to further coupling (translation/determination unit) 41 and into a quantization control signal which is fed via quantization output 8 to the control input of inverse quantization means 10 and to input 18 of control unit 17. VLD 9 performs a decoding, for example on the basis of a table, after which inverse quantization means 10 perform an inverse quantization. In this way, a data signal is obtained at output 15 which is the difference between a particular group of picture elements, or pixels, of a particular picture and a particular group of pixels of a picture preceding it. Said data signal is therefore still in a certain transformation domain as a consequence of the lack of inverse transformation means and thus represents a video signal which is still to some extent coded. From output 15, said data signal flows to input 29. The quantization means 32 quantize said data signal. The quantized signal is then coded by VLC 33, for example on the basis of a table with which new code words are generated which have, on average, a shorter length than the incoming words, and is fed to input 25 of multiplexer 22. This quantized and coded signal is then converted, together with the quantization control signal computed by control unit 17, the unmodified vector signal and any adjusted mode signal, into the second coded bit stream by multiplexer 22.

According to this transcoder, the second coded bit stream represents the video signal in accordance with the same standard as the first coded bit

stream, but at a lower bit rate.

Compared with the transcoder shown in Fig. 1, further coupling 40, or translation unit 40, is lacking in the transcoder shown in Fig. 2 because the vector signal from vector output 6 does not need to be converted into another vector signal for vector input 26 as a consequence of the fact that no translation from one standard into the other standard is necessary, and no adjustment of the accuracy and/or range of the vector information is necessary, and that no vector signals need to be combined to form one vector signal. Further coupling 41, or translation/determination unit 41, is, however, still present in the transcoder shown in Fig. 2 because, while the same standard has been retained, data processing means 23 may nevertheless make a mode change necessary, in which case they drive translation/determination unit 41, via a connection which is not shown in Fig. 2.

## Claims

1. Transcoding device comprising a decoding section for
  - receiving a first coded bit stream, and
  - converting the first coded bit stream into at least one data signal,
 and comprising a coding section for
  - receiving at least one data signal, and
  - converting at least one data signal into a second coded bit stream,
 which transcoding device has a coupling, situated between the decoding section and the coding section, for transporting at least one data signal, characterized in that the transcoding device has a further coupling, situated between the decoding section and the coding section, for transporting at least one information signal.
2. Transcoding device according to Claim 1, characterized in that the decoding section is provided with
  - data reprocessing means for generating a data signal, and
  - demultiplexing means having an input for receiving the first coded bit stream and having a first output coupled to the data reprocessing means and having a second output for generating an information signal,
 the coding section being provided with
  - data processing means for receiving a data signal, and
  - multiplexing means having a first input coupled to the data processing means and having a second input for receiving an information signal and having an out-

put for generating the second coded bit stream,  
the coupling being disposed between the data reprocessing means and the data processing means, and the further coupling being disposed between the second output of the demultiplexing means and the second input of the multiplexing means.

3. Transcoding device according to Claim 2, characterized in that the data reprocessing means comprise a control input which is coupled to the second output, and the data processing means comprise a control input which is coupled to the second input. 15

4. Transcoding device according to Claim 2 or 3, characterized in that the data reprocessing means comprise decoding means and inverse quantization means, and the data processing means comprise further quantization means and coding means. 20

5. Transcoding device according to Claim 4, characterized in that the data reprocessing means comprise inverse transformation means and prediction means disposed in a feedback loop, and the data processing means comprise (further) transformation means and (further) prediction means disposed in a feedback loop containing therein inverse further quantization means and inverse (further) transformation means. 25

6. Transcoding device according to Claim 2, 3, 4 or 5, characterized in that the information signal is a quantization control signal, the second output being a quantization control signal output and the second input a quantization control signal input. 30 35

7. Transcoding device according to Claim 2, 3, 4 or 5, characterized in that the information signal is a vector signal, the second output being a vector output and the second input a vector input. 40 45

8. Transcoding device according to Claim 2, 3, 4 or 5, characterized in that the information signal is a mode signal, the second output being a mode output and the second input a mode input. 50

9. Transcoding device according to Claim 2, 3, 4 or 5, characterized in that the information signal comprises a vector signal and a mode signal, the second output comprising a vector output and a mode output and the second 55

input comprising a vector input and a mode input.

10. Transcoding device according to Claim 2, 3, 4 or 5, characterized in that the information signal comprises a quantization control signal, a vector signal and a mode signal, the second output comprising a quantization control signal output, a vector output and a mode output and the second input comprising a quantization control signal input, a vector input and a mode input.

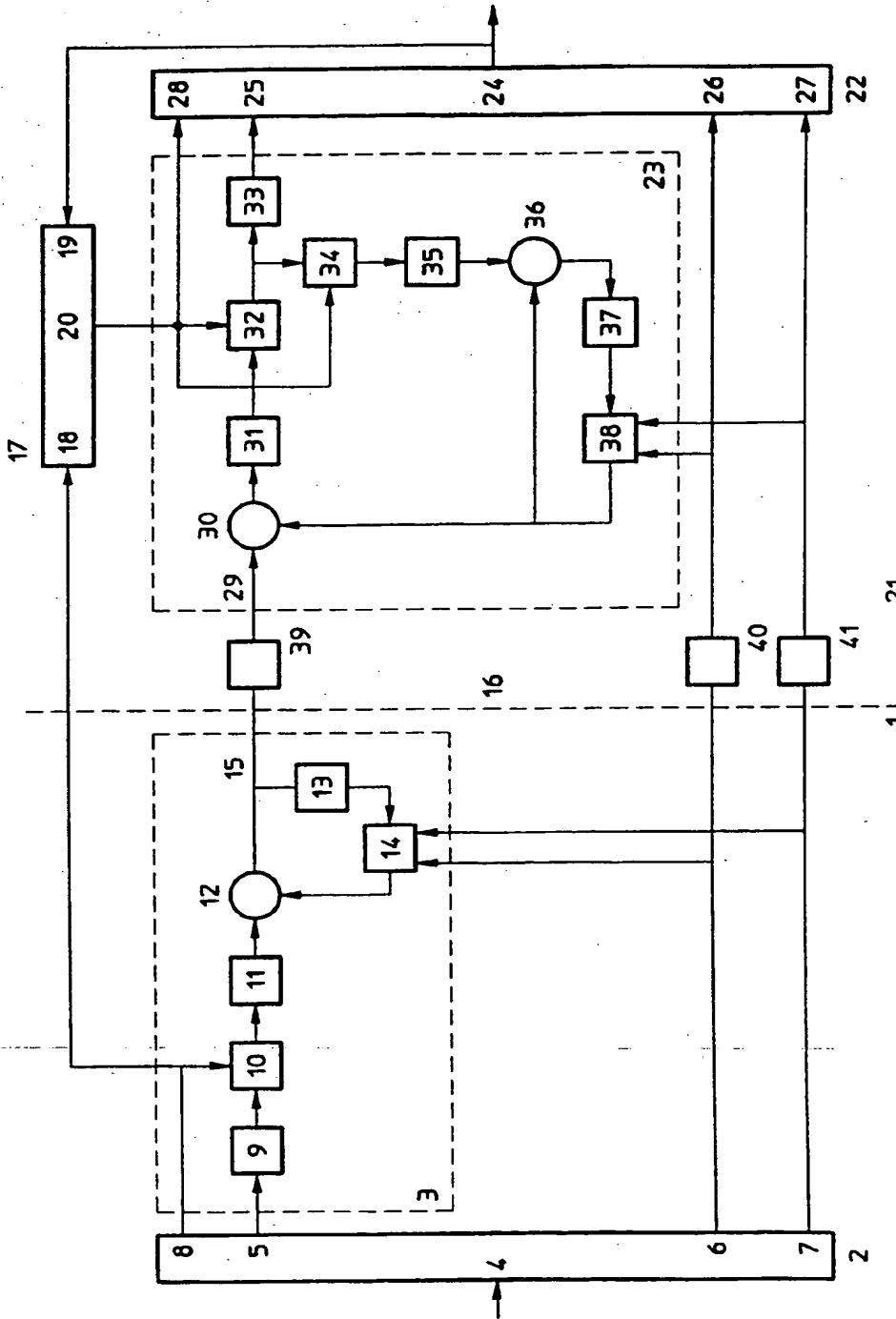


Fig. 1

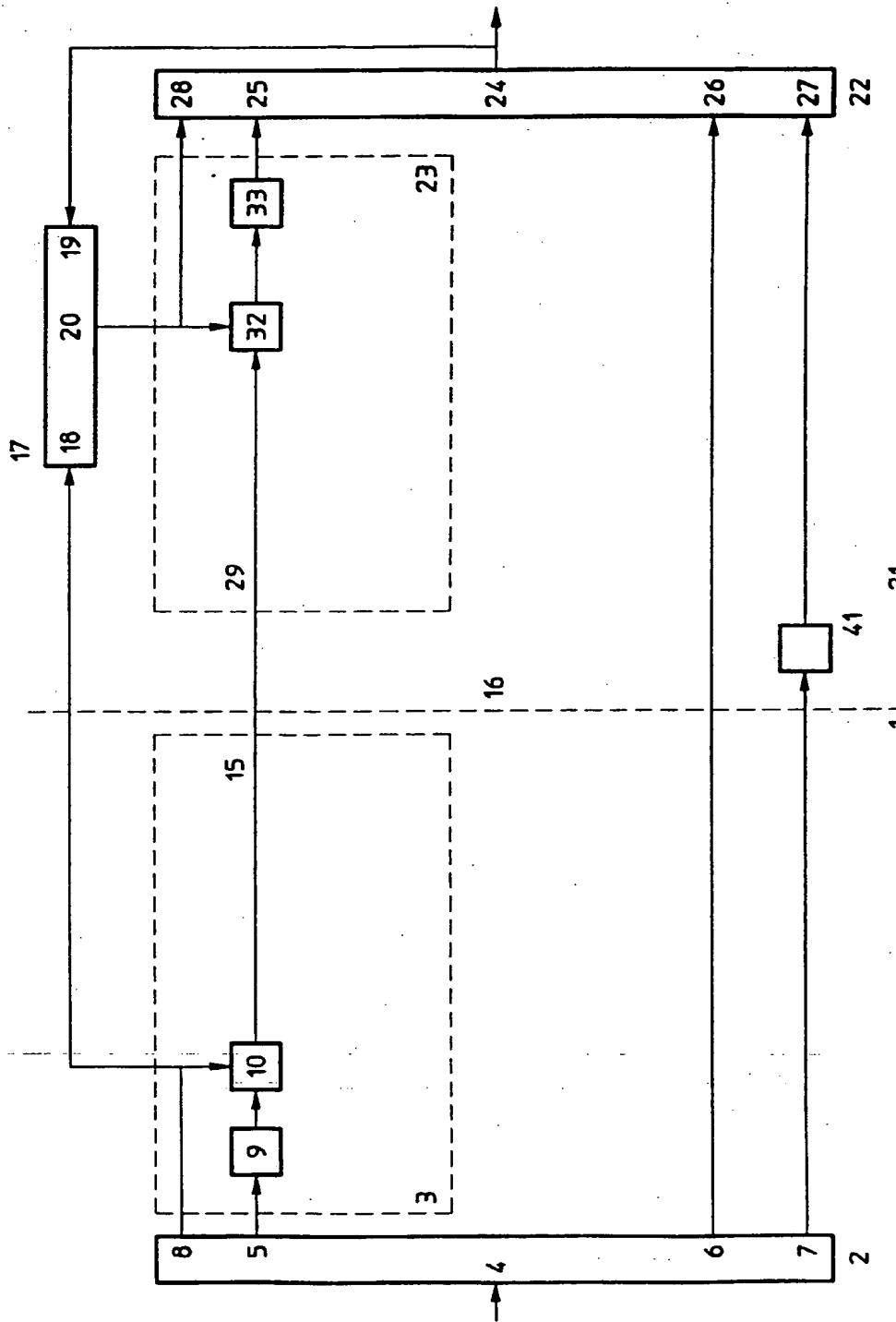


Fig. 2



European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 94 20 2246

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
A	COMMUNICATIONS OF THE ACM, vol.34, no.4, 1 April 1991, NEW YORK, USA pages 103 - 112, XP228793 M. LIEBOLD ET AL. 'Toward an Open Environment for Digital Video' * page 107, column 1, line 43 - column 2, line 14; figure 2 *	1-10	H04N7/26 H04N7/30 H04N7/32
A	PATENT ABSTRACTS OF JAPAN vol. 16, no. 520 (P-1444) 26 October 1992 & JP-A-04 192 080 (MITSUBISHI ELECTRIC CORP.) 10 July 1992 * abstract *	1-10	
A	PATENT ABSTRACTS OF JAPAN vol. 7, no. 10 (P-168) 14 January 1983 & JP-A-57 166 640 (HITACHI SEISAKUSHO KK) 14 October 1982 * abstract *	1-10	
A	BROADCAST SESSIONS. 17TH INTERNATIONAL TELEVISION SYMPOSIUM, 13 June 1991, MONTREUX, CH pages 552 - 558, XP268913 B. PANK 'Picture Conversion for HD Graphics' * the whole document *	1-10	TECHNICAL FIELDS SEARCHED (Int.Cl.)  H04N
A	BROADCAST SESSIONS. 18TH INTERNATIONAL TELEVISION SYMPOSIUM, 10 June 1993, MONTREUX, CH pages 432 - 447, XP385307 M.W.A. DAVID ET AL. 'A Third Generation Algorithm for Use in High Definition Frame Rate Convertors' * the whole document *	1-10  -/-	
The present search report has been drawn up for all claims			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	11 November 1994	Foglia, P	
CATEGORY OF CITED DOCUMENTS			
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## EUROPEAN SEARCH REPORT

Application Number  
EP 94 20 2246

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.)
A	EBU REVIEW TECHNICAL, no.244, 1 December 1990, BRUSSELS, BE pages 217 - 228, XP176039 C. DOSCH 'Direct Satellite Broadcasting of Digital HDTV in the 20 GHz Frequency Range' * paragraph 2.3; figure 2 * -----	1,2	
A	EP-A-0 500 077 (NEC CORP.) * abstract; figure 1 * -----	1-6	
<b>TECHNICAL FIELDS SEARCHED (Int.Cl.)</b>			
<b>The present search report has been drawn up for all claims</b>			
Place of search <b>THE HAGUE</b>	Date of completion of the search <b>11 November 1994</b>	Examiner <b>Foglia, P</b>	
<b>CATEGORY OF CITED DOCUMENTS</b>		<p>T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons A : member of the same patent family, corresponding document</p>	
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